

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Matthew B. Haycock et al.

METHOD AND APPARATUS FOR REALIGNING BITS ON A PARALLEL BUS

Docket No.:

884.455US1

Serial No.: 09/964,010

Filed:

September 26, 2001

Due Date: May 23, 2005

Examiner:

Tse W. Chen

Group Art Unit: 2116

## **MS** Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

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X An Amendment and Response (17 Pages).

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

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<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this <u>23rd</u> day of May, 2005.

Name

Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

S/N 09/964,010 AND S/N 09/964,010

**PATENT** 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew B. Haycock et al.

Examiner: Tse W. Chen

Serial No.:

09/964,010

Group Art Unit: 2116

Filed:

September 26, 2001

Docket No.: 884.455US1

Title:

METHOD AND APPARATUS FOR REALIGNING BITS ON A PARALLEL

BUS

Assignee:

Intel Corporation

Customer No: 21186

## **AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This paper responds to the Office Action mailed on <u>February 23, 2005</u>. Please amend the above-identified patent application as follows.